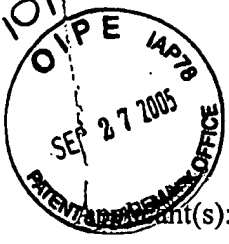


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PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Harsh D. Sharma et al.
Title: PRIORITY DELAY INSERTION CIRCUIT
Application No.: 09/925,993 Filed: August 9, 2001
Examiner: David B. Lugo Group Art Unit: 2637
Atty. Docket No.: 004-6183 Confirmation No.: 4755

September 27, 2005

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE AFTER FINAL REJECTION (37 C.F.R. § 1.116)

This paper is being submitted following the Final Rejection mailed on August 9, 2005.
In light of the Amendments and/or Remarks herein, further consideration is requested.

09/29/2005 TBESHAHI 00000021 09925993
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